

METHOD AND APPARATUS FOR MAINTAINING DATA DENSITY FOR DERIVED CLOCKING

5

ABSTRACT OF THE DISCLOSURE

10

Some embodiments of the invention implement point-to-point memory channels that virtually eliminate the need for mandatory synchronization cycles for a derived clocking architecture by tracking the number of data transitions on inbound and outbound data lanes to make sure the minimum number of transitions occur. Other embodiments of the invention perform data inversions to increase the likelihood of meeting the minimum data transition density. Still other embodiments are described in the claims.